MODBUS/TCP CONTROLLER FOR THE POWER SUPPLIES IN ALS BTS BEAM LINE

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Abstract
The development, testing and commissioning of a self-contained power supply controller for four 100 kW power supplies for the upgraded Booster to Storage Ring (BTS) beam line at the Advanced Light Source (ALS) at the Lawrence Berkeley Laboratory, is presented. The power supply controller is a 3U high, rack-mount chassis that contains the regulation control-loop amplifiers, 16-bit DAC with microcontroller and a micro PLC (Programmable Logic Controller) for power-supply state-machine control. Local control is achieved via push-buttons and a color LCD touch screen. Remote control is mediated via micro PLC using embedded Modbus/TCP. Using a unique, data logging system, the operational parameters of the regulation loop can be safely monitored and recorded while the system is running at full power. The entire design is based on optimum reliability, safety and ease of troubleshooting and repair. A modular design for key control components, allows the power supply to operate in a nominal mode, even if one or two ancillary internal modules fail. This allows for continued beam operation until it is convenient to service the unit, keeping beam availability as high as possible.

CONTROLLER DEVELOPMENT
The Advanced Light Source at the Lawrence Berkeley Laboratory has recently commissioned an upgrade to allow continuous “top off” of the main storage ring. The upgrade required new magnet power supplies for the Booster to Storage Ring (BTS) beam line [1, 2].

The four 100 kW power supplies were required to be operated both locally and remotely via the ALS’ EPICS control system. The ALS specified Modbus/TCP [3] as the remote control protocol.

A team from Alpha Scientific Electronics and Bira Systems designed a controller that performs closed-loop current regulation, manages the power-supply’s state machine, provides a local user interface and manages the Modbus remote communications. To facilitate commissioning, a power-supply simulator was supplied to the controls team at the ALS so EPICS programming could proceed in parallel with the building of the four power supplies. The team had used this strategy for the 1 MW Dipole power supply used successfully at the Australian Synchrotron.[4]

The design goals for the team were three fold.
1. High current stability, 100ppm regulation with critically damped response to step changes in reference input. The ability to easily adjust feedback compensation for different loads
2. Modularity and scalable design of command structure and control components.
3. Fail-safe operation that will allow local operation if the more complex remote-control interface fails.

CONTROLLER HARDWARE
Controls are housed in a 3U 19” chassis. The operator interface consists of a Windows-CE-based touch screen, control buttons and local set-point adjustment. See Figures 1 & 2.

Figure 1: Actual Control Panel

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FUNCTIONAL BLOCKS

A modular design for key control components, allows the power supply to operate in a nominal mode, even if one or two ancillary internal modules fail. This allows for continued beam operation until it is convenient to service the unit, keeping beam availability as high as possible.

All of the processes are managed by a MicroSmart PLC. A programmable 16-bit reference signal is fed to the regulator circuit, with an on-board RS-232 communications link to a Windows CE 3 (WinCE) single board computer and touch screen. The 6” display simulates volt and ammeters, and displays operating and fault states of the power supply. See Figure 2.
Windows CE touch screen is a communications interface/conduit. It syncs itself to the PLC via a serial (RS-232) link, COM1 on the WinCE machine. All physical buttons and interlocks are attached to the PLC. On and off sequencing of contactors, SCR drivers, regulator is handled solely by the PLC. The WinCE can “die” and the power supply will still function.

The IDEC microSmart PLC built-in Modbus RTU serial capability [5]. Modbus TCP uses a different encoding scheme and checksum algorithm for encoding the Modbus commands. A MOXA[6] serial to TCP/IP Modbus protocol converter completes communication link to the ALS EPICS system.

**MODBUS COMMAND/REGISTER SET**

A consequence of using the Modbus[3] protocol is the convenience of programming from the PLC-side of

<table>
<thead>
<tr>
<th>Command</th>
<th>Register</th>
<th>Description</th>
<th>Argument/Response</th>
</tr>
</thead>
<tbody>
<tr>
<td>03</td>
<td>400257</td>
<td>Query Actual Output Current</td>
<td>Unsigned 16-bit value</td>
</tr>
<tr>
<td>03</td>
<td>400259</td>
<td>Query Ground Current</td>
<td>Unsigned 16-bit value</td>
</tr>
<tr>
<td>03</td>
<td>401001</td>
<td>Status of the power supply and interlock fault bits.</td>
<td>Two 16-bit registers with interlock and status information ON/OFF RDY/NRDY REMOTE/NREMOTE INTERNAL/EXTERNAL reference</td>
</tr>
<tr>
<td>03</td>
<td>400258</td>
<td>Query Voltage</td>
<td>Unsigned 16-bit value</td>
</tr>
<tr>
<td>03</td>
<td>400300</td>
<td>Watchdog Register</td>
<td>Unsigned 16-bit value</td>
</tr>
<tr>
<td>06</td>
<td>400001</td>
<td>Set Current Set Point</td>
<td>Expect an unsigned 16 bit value</td>
</tr>
<tr>
<td>06</td>
<td>401901</td>
<td>Turn power supply on off.</td>
<td>ON / OFF 0x0001 or 0x0000</td>
</tr>
<tr>
<td>06</td>
<td>401902</td>
<td>Reset to clear interlock faults</td>
<td>Put supply in OFF-READY state if all faults have cleared. Send 0xFF01 to reset.</td>
</tr>
<tr>
<td>06</td>
<td>401910</td>
<td>Keep alive Register</td>
<td>Expect an unsigned 16 bit value</td>
</tr>
</tbody>
</table>

**COMMISSIONING PERFORMANCE**

Once AC connections were completed, EPICS control was established within a few hours of power up. Fine adjustments for regulation compensation and “tweaks” to the EPICS interface continue during scheduled accelerator downtimes. All four power supplies are functioning within specifications.

**REFERENCES**

[1] Barry, Walter; Chin, Mike; Robin, David; Sannibale, Fernando; Scarvie, Tom; Steier, Christoph, “Diagnostic systems plan for the advanced light source top-off upgrade”. Proceedings of the Particle Accelerator Conference, PAC 2005, June 2005, p 4066-4068